

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



re application of: Sanish Koshi JACOB

Appl. No.: 10/710692

Filed: July 28, 2004

For: High Voltage Circuits implemented Using Low

Voltage Transistors

Art Unit: UNASSIGNED

Examiner: UNASSIGNED

Atty. Docket: TI-37611

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents Alexandria, VA 22313-1450

Sir:

Listed below on an attached Form PTO-1449 is information known to applicant(s). A copy of each listed publication and U.S. and foreign patent, except for pending U.S. applications, is being submitted herewith, along with a concise explanation of information in a foreign language, if any, pursuant to 37 C.F.R. §1.97-1.98.

Applicants respectfully request that the listed information be considered by the Examiner and be made of record in the above-identified application. The Examiner is requested to initial and return it in accordance with MPEP §609.

This statement is not intended to represent that the information cited in the statement is, or is considered to be, material to patentability as defined in §1.56. Applicant reserves the right to establish patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information is not enabling for the teachings purportedly offered.

Appl. No.: 10/710692 Inventor: Sanish Koshi JACOB

- X This statement qualifies under 37 C.F.R. $\S 1.97$, subsection (b) because (check all that apply):
 - <u>∠</u> (1) It is being filed within 3 months of the application filing date; OR
 - (2) It is being filed within 3 months of entry of a national stage; OR
 - X (3) It is being filed before the mail date of the first Office Action on the merits.
 - 37 C.F.R. §1.97(c). If this statement is being filed after the latest of: (1) three months beyond the filing date of a national application; (2) three months beyond the date of entry of the national stage as set forth in §1.491 in an international application; or (3) the mailing date of a first Office action on the merits, but before the mailing date of the earlier of a final office action under §1.113 or a notice of allowance under §1.311, then:
 - a certification as specified in §1.97(e) is provided below; or
 - a fee of \$180.00 as set forth in \$1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.
 - 37 C.F.R. $\S1.97(d)$. If this statement is being filed after the mailing date of the earlier of a final office action under $\S1.113$ or a notice of allowance under $\S1.311$, but before payment of the issue fee, then:
 - A. a certification as specified in §1.97(e) is submitted herewith; and
 - B. a petition under 37 C.F.R. §1.97(d) requesting consideration of this statement is submitted herewith; and
 - C. a fee of \$130.00 as set forth in \$1.17(i)(1) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.

Respectfully submitted,

Narendra Reddy Thappeta

Registration Number: 41,416

Date: July 30, 2004

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	PLICANT	First Named Inventor	Sanish koshy jacob	HOD 0 3 2004 1		
STATEMENT BY APPLICANT				Group Art Unit	UNASSIGNED	·
	(use as many sheets	as nec	essary)	Examiner Name	UNASSIGNED	Nr. at
Sheet	1	of	2	Attorney Docket Number	TI-37611	RADEMA

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.¹	U.S. Patent Document Number		Name of Patentee or Applicant of Cited Document	Date of Publication of cited Document	Pages, Columns, Lines, Where Relevant
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		4,704,547		Kirsch; Howard C.	11-03-1987	
					 	
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Substitute for form 1449B/PTO Complete if Known **Application Number** 10/710,692 INFORMATION DISCLOSURE **Filing Date** July 28, 2004 STATEMENT BY APPLICANT **First Named Inventor** Sanish koshy jacob **Group Art Unit** <u>UNASSIGNED</u> (use as many sheets as necessary) **UNASSIGNED Examiner Name** Sheet 2 of 2 **Attorney Docket Number** TI-37611

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Examin er Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Hector Sanchez, Joshua Siegel, Carmine Nicoletta, James .p and Nissen ,jose Alvarez , "A Versatile 3.3/2.5/1.8-V CMOS I/O Driver Bulit in a 0.2-um ,3.5-nm Tox, 1.8-V CMOS Technology " in IEEE Journal of Solid State Circuits Vol 34. pp. 1501-1511, No.11, November 1999.	
		Changsik Yoo, Min -Kyu Kim , and Wonchan kim , " A Static Power Saving TTL-to -CMOS Input buffer " in IEEE Journal of Solid State Circuits. Vol 30, pp. 616- 620, No. 5, may 1995.	
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